

ACKNOWLEDGEMENT RECEIPT

Electronic Version

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Title of Invention	METHOD FOR FABRICATING A SEMICONDUCTOR TRANSISTOR DEVICE HAVING ULTRA-SHALLOW SOURCE/DRAIN EXTENSIONS
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Submission Type : Utility Patent Filing

Application Number:

10/710521



EFS ID:

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First Named Applicant: Yun-Ren Wang

Attorney Docket Number: NAUP0591USA

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File Listing:

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us-assignment	NAUP0591_0_A1-usassn.xml	2064	2004-07-18
us-assignment	us-assignment.xsl	8983	2004-07-18
us-assignment	us-assignment.dtd	10898	2004-07-18
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us-assignment	NAUP0591_0_A1ASS2.tif	89970	2004-07-18
us-request	NAUP0591_0_A1-usrequ.xml	2909	2004-07-18
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us-fee-sheet	NAUP0591_0_A1-usfees.xml	2130	2004-07-18
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us-power-of-attorney-grant	NAUP0591_0_A1-uspoat.xml	1049	2004-07-18
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us-declaration	NAUP0591_0_A1DEC2.tif	77582	2004-07-18
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application-body	us-application-body.xsl	83497	2004-07-18
application-body	application-body.dtd	49498	2004-07-18
application-body	wipo.ent	4956	2004-07-18

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application-body	Naup0591usa-03.tif	5014	2004-07-18
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package-data	us-package-data.xsl	19263	2004-07-18
Total files size			970819

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